

# Monolithic Driver in Smart Power Technology

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September 19<sup>th</sup>, 2024

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**External Paper Validation N. ID 1934** 







# BCD Technology and IEEE Milestone recognition in May 2021

A concept invented by ST in the mid-80s,

widely used today in the industry









## Example of a BCD product

High voltage & power section (DMOS) to drive external loads

Memory: RAM NVM: ePCM (embedded Phase Change Memory)

Analog blocks to sense the external world

Digital core (CMOS) for signal processing



# Why power density matters?

#### More power in less space thanks to BCD



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Imagine to save just 1 W\*h per each phone recharge cycle. Multiply it for 1 Billion units every day

Since beginning of electronics era, users' needs have dictated a clear trend toward **size and weight reduction** of electronic equipment



Reducing size and weight can **cut total cost of ownership** by making installation and maintenance both easier and quicker



**Portability** needs high power density. Smaller, lighter, yet more powerful (shorter recharge time): this is perceived by users as added value

## BCD technology portfolio

0.32 µm	BCD6 / 6s $20 \vee -100 \vee$ BCD6s-OFFLINE $650 \vee -800 \vee$ $650 \vee -650 \vee$ BCD6s-SOI $100 \vee -190 \vee$ Galvanic Isolation (Inductive) $4 \text{ kV} - 6 \text{ kV}$	ECD1200 1200 V Galvanic Isolation (Capacitive)	Galvanic Isolation (Capacitive) [° 10 kV °]
0.16 µm	BCD8s-AUTO BCD8sP BCD8s-SO   40 V - 100 V 5 V - 60 V 100 V - 200 V		MONOLITHIC GaN GaN
0.11 µm	BCD9s 5 V − 100 V GaN →	00V BCD110AP_110V	SOIBCD110_180V+
90 nm	High density		
40 nm	<ul><li>High power</li><li>High voltage</li></ul>	<b>BCD40</b> 5 V – 40 V	
	Available	Prototyping	Development

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### History of microprocessor and POWER in BCD technology



### **BCD9s Demo Main features**

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#### 30 Ampere version



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### Monolithic Solution for motor control





### System in Package: Smart Motor Driver + 8 Power MOS for 30A current capability









- Hardware accelerator and automatic closed control loop for synchronous motor driving
- Already proven the integration as STM32 plug & play peripheral
- The peripheral manages all the motor signals leaving to the uP only the high-level tasks
- High efficiency due to adaptive driving
- Very low speed rotation ripple
- More than 40dB attenuation in acoustic noise
- 25 patents on SMOOTH driving
- Motor life is increased by SMOOTH driver minimizing the mechanical vibration and solicitation on the baring

Smooth-drive is a digital system for the sinusoidal driving of the motor. The voltage profile is programmable according to the motor to match the Back Electro Magnetic Force profile.







тм

### CORTEX-M4+ePCM+PWR in BCD9s: DEMO Videos





### Monolithic solution for smart motor-drive - Demo



Single microcontroller directly soldered on board and directly connected to the main battery (supports up to 35V)

All voltage supplies (1.8V, 3.3V, 5V) self generated and direct connection from the chip to the 3 BLDC phases

Automatic synchronous control of the rotor spinning thanks to motor control hardware accelerator

Cortex-M4 processor and PCM non-volatile memory to run custom firmwares for high-level task, e.g. stroboscopic LED driving, synchronous with rotor spinning (the white label on the motor seems to be frozen).



### Conclusion

#### The SMART POWER DRIVING...



- ... simplifies the system hardware
- ... improve the control precision
- ... is feasible in monolithic solution
- ... improves the control loop bandwidth

... reduces drastically the dimensions especially for small motors or actuators

- ... optimizes the power consumption
- ... minimize the connection wires

... Any questions? ...

. . .

# Our technology starts with You



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